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JUL 132006 ATTORNEY DOCKET NO. 10031350-1

AGILENT TECHNOLOGIES, INC. Legal Department, DL429 Intellectual Property Administration P. O. Box 7599 Loveland, Colorado 80537-0599

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Hildebrant

Serial No.: 10/815,521 Examiner: Doan, Nghia M

Filing Date: April 1, 2004

Group Art Unit: 2825

TITLE: VERIFICATION OF INTEGRATED CIRCUIT TESTS USING TEST SIMULATION AND

INTEGRATED CIRCUIT SIMULATION WITH SIMULATED FAILURE

COMMISSIONER FOR PATENTS P.O. Box 1450

Alexandria VA 22313-1450

TRANSMITTAL LE	TTER FOR RE	SPONSE/AMENDMENT
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Sli	r.														
Tra	Transmitted herewith is/are the following in the above-identified application:														
×	Response	/Amendment		Petition to extend time to respond											
	New fee a	s calculated below		Supplemental Declaration											
×	No additional fee (Address envelope to "Mail Stop Amendments")														
	Other: (Fee \$)														
١	CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY														
	(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	HIGHEST NUMBER PREVIOUSLY PAID FOR 37		(5) PRESENT EXTRA		(6) RATE		(7) ADDITIONAL FEES					
	TOTAL CLAIMS	30	MINUS			ı	0	х	50	\$	0				
	INDEP. CLAIMS	3	MINUS	3			0	×	200	\$	0				
	FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM + 360									\$	0				
	EXTENSION 1 ST MONTH 2 ^{NO} MONTH FEE 120.00 ☐ 450.00 ☐					4 TH M 1590.	MONTH 90.00		\$	0					
OTHER FEES									\$	0					
ì	TOTAL ADDITIONAL FEE FOR THIS AMENDMENT									\$	0				

to Deposit Account 50-1078. At any time during the pendency of this application, please charge Charge \$ 0 any fees required or credit any over payment to Deposit Account 50-1078 pursuant to 37 CFR 1.2 5. Additionally please charge any fees to Deposit Account 50-1078 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this transmittal letter is enclosed.

Respectfully submitted,

Hildebrant

Ву

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below:

Date of facsimile: 07-13-2008

Typed Name: Jessica Costa

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Ray 05/05 (TransAmd)

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

PATENT APPLICATION: Serial No. 10/815,521

ORIGINALLY FILED: April 1, 2004

FOR:
VERIFICATION OF INTEGRATED
CIRCUIT TESTS USING TEST
SIMULATION AND INTEGRATED
CIRCUIT SIMULATION WITH
SIMULATED FAILURE

INVENTOR: Hildebrant, Andrew

GROUP ART UNIT: 2825

EXAMINER: Doan, Nghia M

DOCKET NUMBER: 10031350-1

SUPPLEMENTARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the phone call from the Examiner on July 13, 2006, please enter the enclosed supplemental amendment to the Supplemental Amendment filed on July 6, 2006 in the above-identified patent application and consider the remarks as follows:

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.

US Patent Application Serial No. 10/815,521 Docket No. 10031350-1